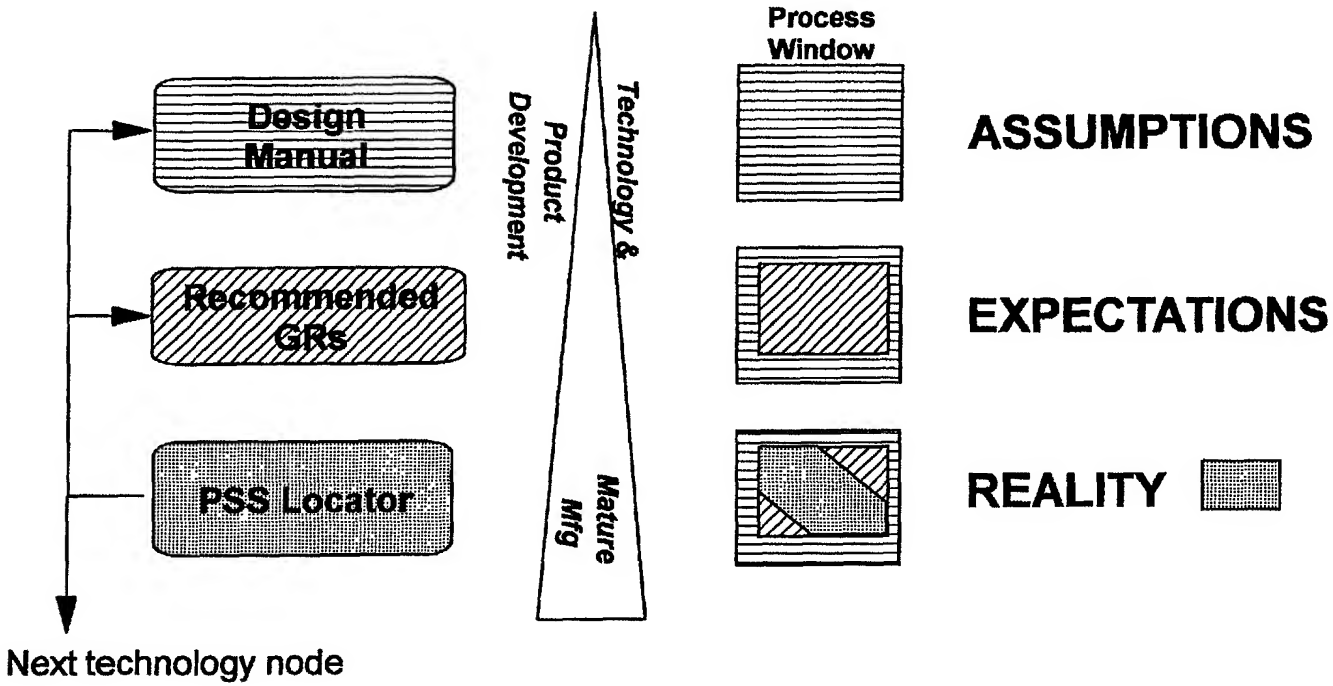
Design Manual
Recommended GRs
PSS Locator
Next technology node
Product Development
Technology & Mfg
Mature
Process Window
ASSUMPTIONS
EXPECTATIONS
REALITY

Fig. 1

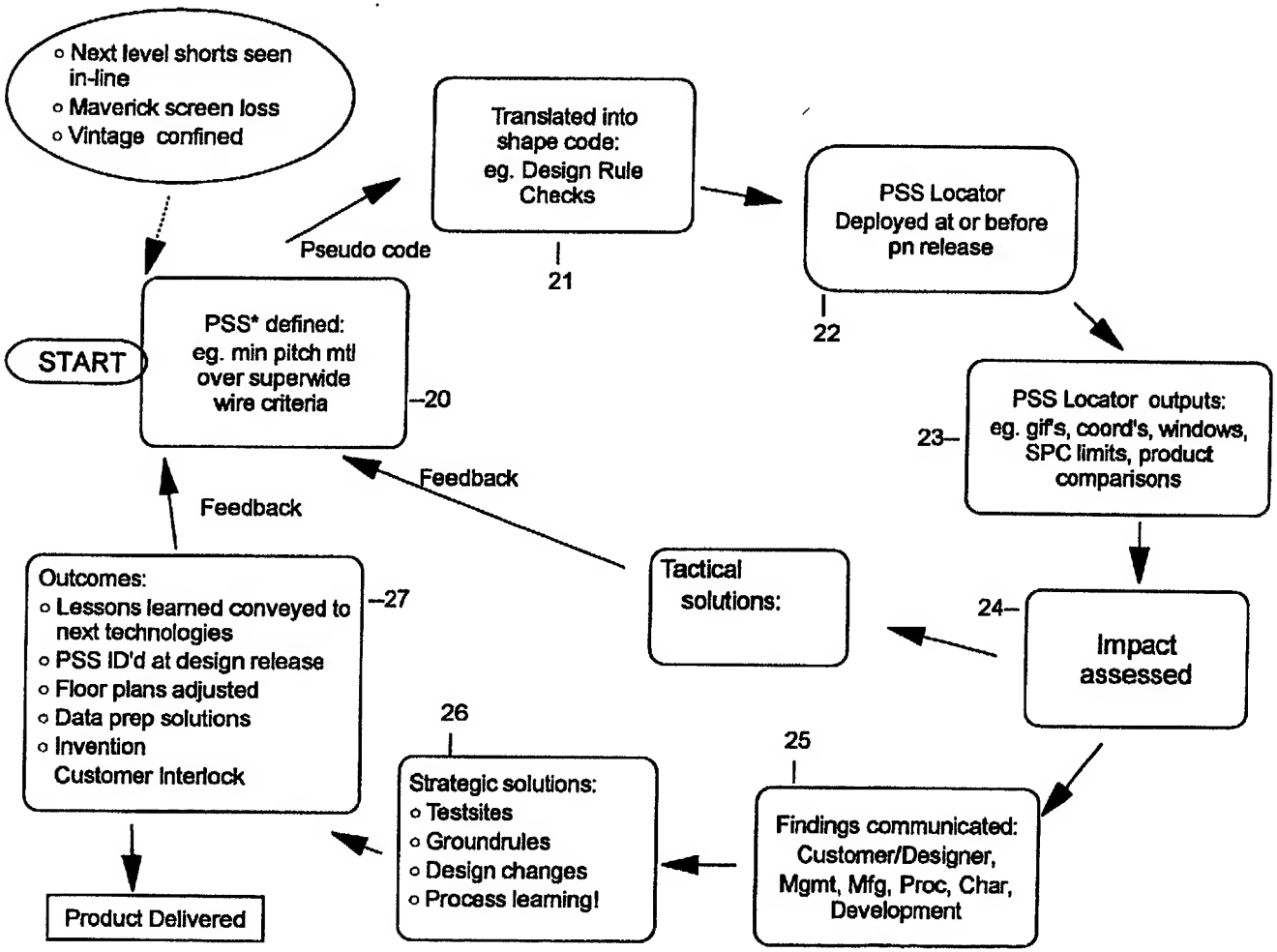

Fig. 2

App_ID=10063427

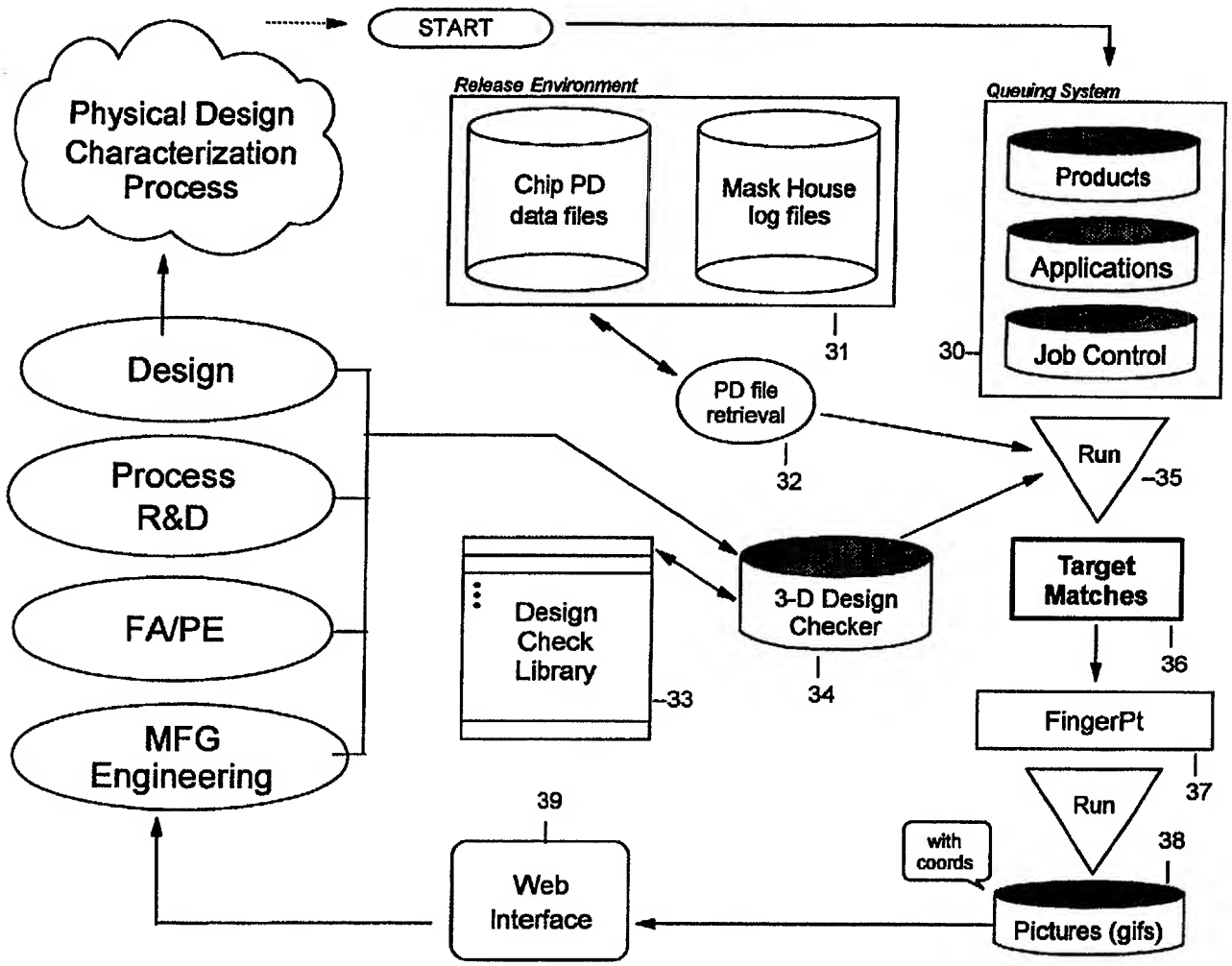

Fig. 3

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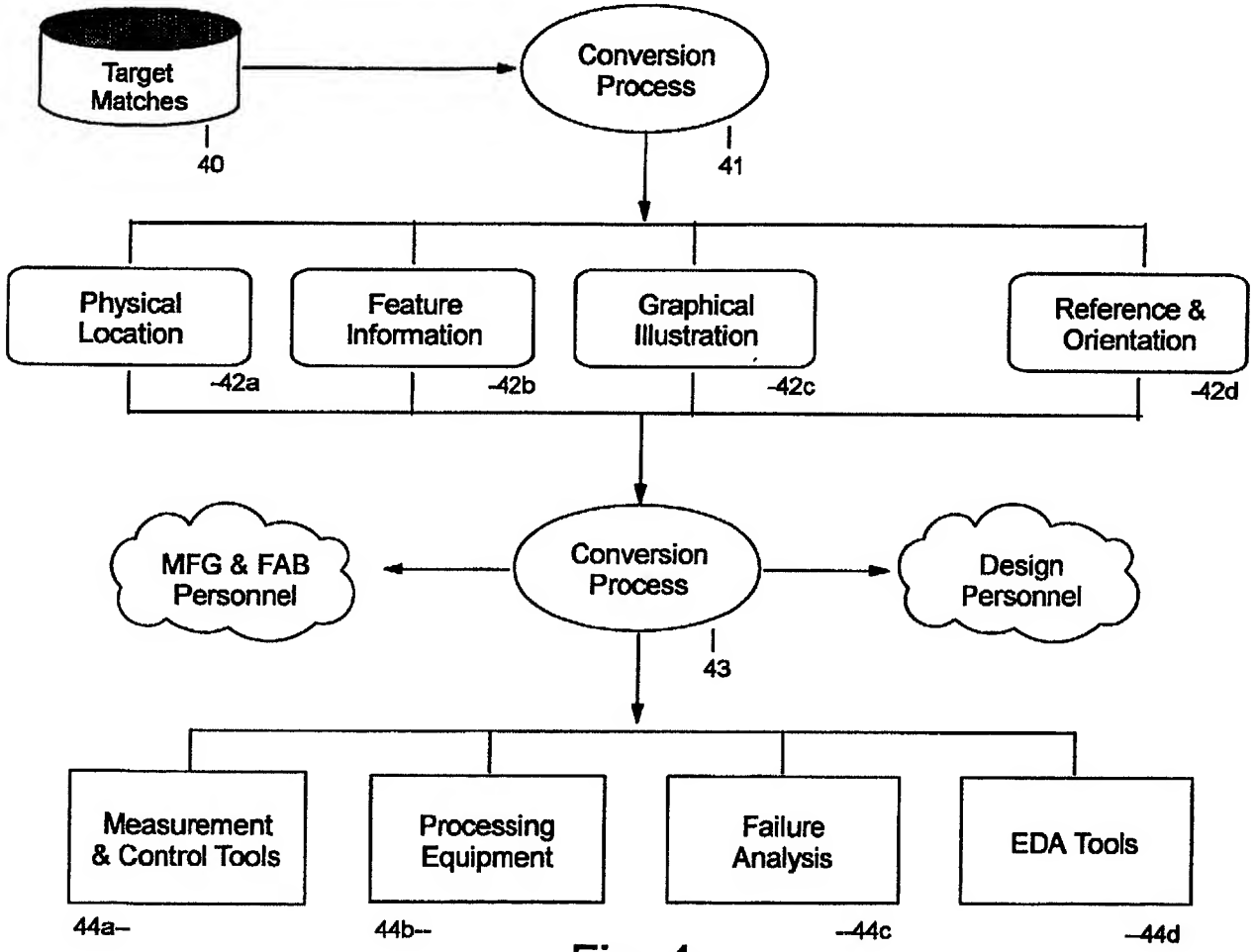

Fig. 4

App_ID=10063427

Extraction data Contents Table

Summary
Map of Windows

Target Matches
  Reference for min spaced M4 over wide spaced M3
    1. (X1,Y1) <specific info1>
    2. (X2,Y2) <specific info2>
    3. (X3,Y3) <specific info3>
  Reference for min spaced M5 over wide spaced M4
    4. (X4,Y4) <specific info4>
    5. (X5,Y5) <specific info5>
  .
  .
  . (etc.)

Fig. 5

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Fig. 6 - Chip Map Reference w/ scale & origin

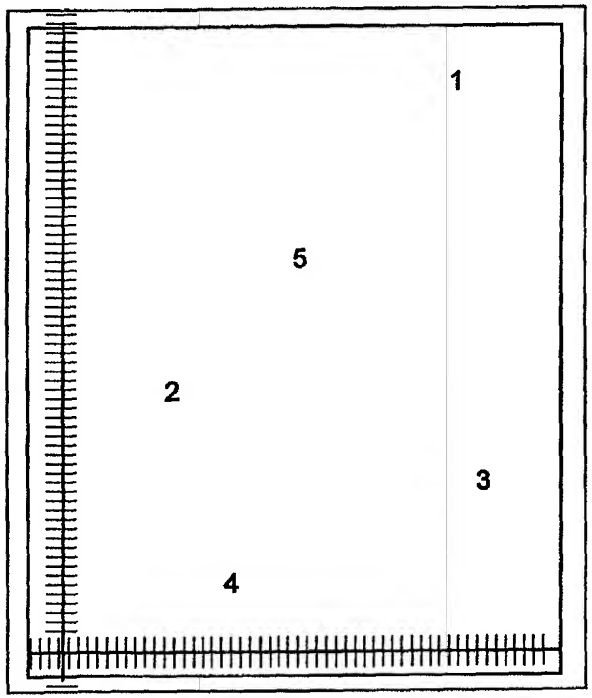

Fig. 7 - Tgt Match Summary

Total number of matches by type:

Min spaced M4 over wide M3: 3

Min spaced M5 over wide M4: 2

Attorney Docket

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Fig. 8 - Chip Origin Reference

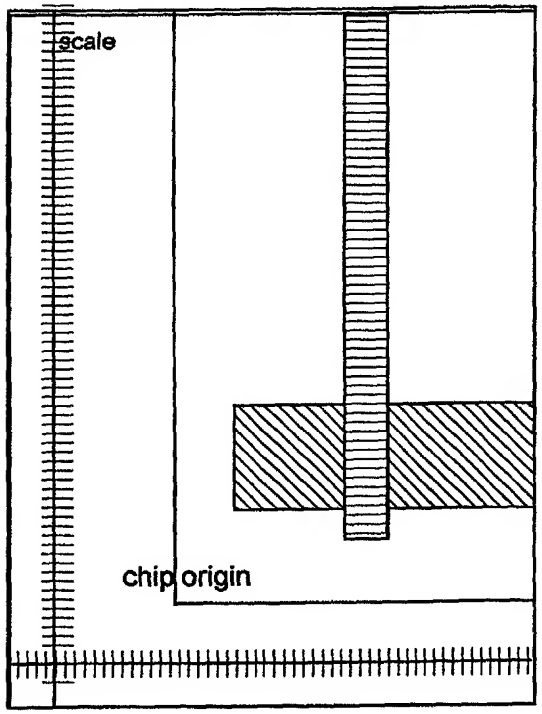
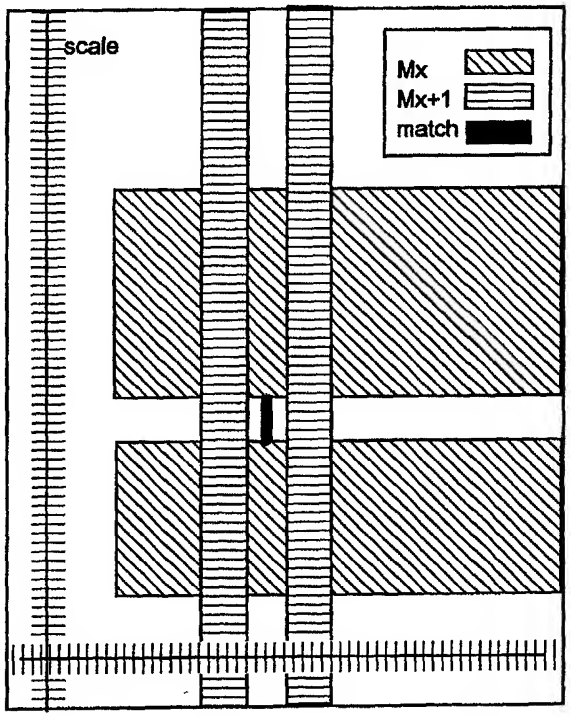


Fig. 9 - PSS Geometry



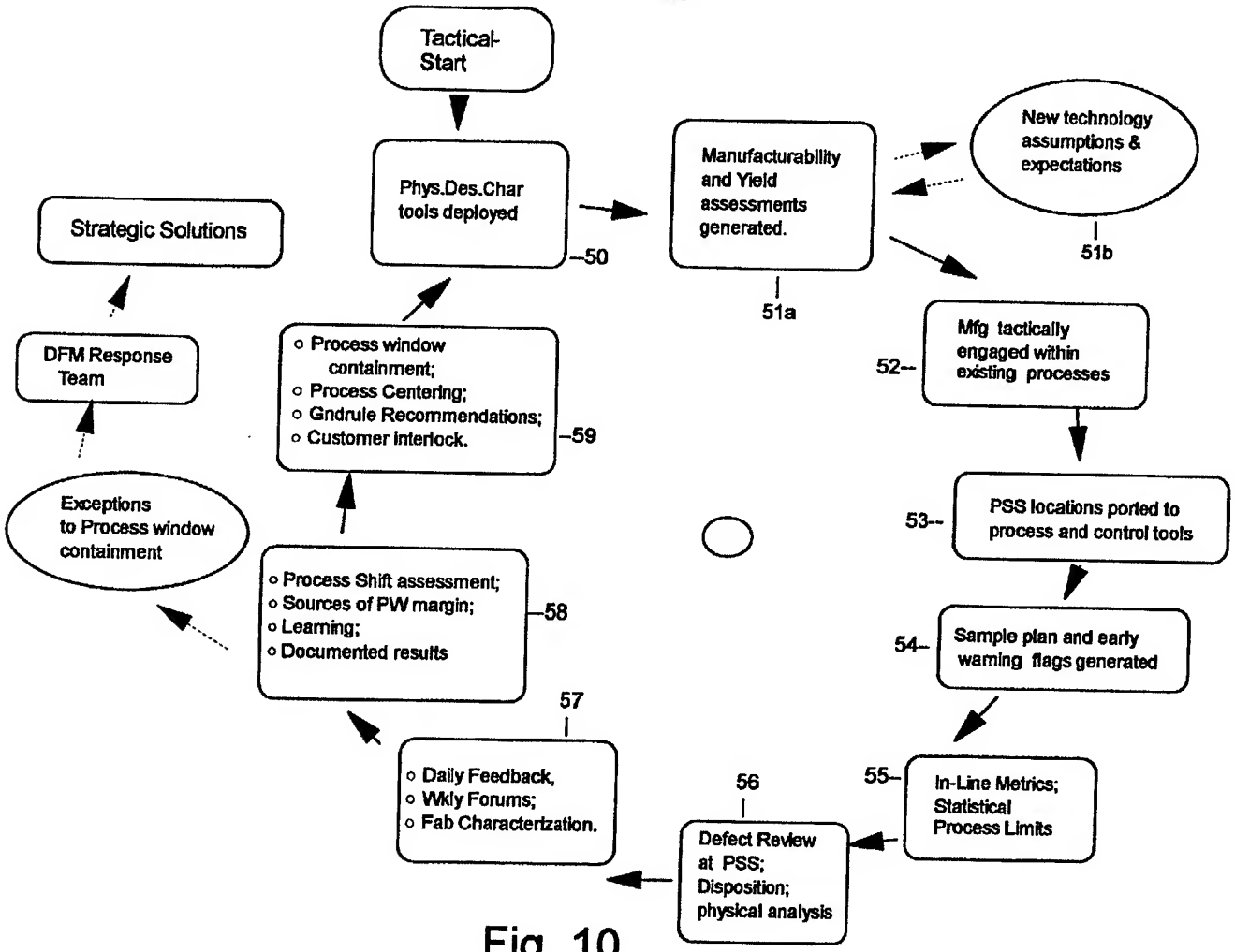


Fig. 10

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